

AMENDMENTS TO THE SPECIFICATION:

Please insert the following on page 1, before line 4:

The present Application is a Divisional Application of U.S. Patent Application No. 09/695,321 filed on October 25, 2000.

Please replace the paragraph on page 11, line 14, with the following amended paragraph:

~~Fig. 6 is a~~ Figures 6(a)-6(d) are cross-sectional view views showing the manufacturing process for an active matrix substrate according to the first embodiment of the present invention.

Please replace the paragraph on page 11, line 17, with the following amended paragraph:

~~Fig. 7 is a~~ Figures 7(a)-7(d) are cross-sectional view views showing the structure of a gate terminal unit according to the first embodiment of the present invention.

Please replace the paragraph on page 11, line 20, with the following amended paragraph:

~~Fig. 8 is a~~ Figures 8(a)-8(d) are cross-sectional view views showing the structure of a drain terminal unit according to the first embodiment of the present invention.

Please replace the paragraph on page 11, line 23, with the following amended paragraph:

~~Fig. 9 is a~~ Figures 9(a)-9(d) are cross-sectional view views showing the structure of a storage capacitor unit according to the first embodiment of the present invention.

Please replace the paragraph on page 12, line 1, with the following amended paragraph:

~~Fig. 10 is a~~ Figures 10(a)-10(d) are cross-sectional view views showing the manufacturing process for an active matrix substrate according to a second embodiment of the present invention.

Please replace the paragraph on page 12, line 16, with the following amended paragraph:

~~Fig. 15 is a~~ Figures 15(a)-15(d) are process diagram diagrams schematically showing the manufacturing process for the active matrix substrate of the third embodiment of the present invention, shown in a cross-section taken along line C-C'.

Please replace the paragraph on page 12, line 20, with the following amended paragraph:

~~Fig. 16 is a~~ Figures 16(a)-16(d) are process diagram diagrams schematically showing the manufacturing process for a storage capacitor of the active matrix substrate of the third embodiment of the present invention, shown in a cross-section taken along line D-D'.

Please replace the paragraph on page 13, line 14, with the following amended paragraph:

~~Fig. 22 is a~~ Figures 22(a)-22(d) are process diagram diagrams schematically showing the manufacturing process for an active matrix substrate according to the fourth embodiment of the present invention, in a cross-section taken along line E-E'.

Please replace the paragraph on page 14, line 8, with the following amended paragraph:

~~Fig. 28 is a~~ Figures 28(a)-28(c) are process diagram diagrams schematically showing the former half of the manufacturing process for a gate bus line and a gate electrode of the active matrix substrate according to the fifth embodiment of the present invention, shown in a cross-section taken along line H-H'.

Please replace the paragraph on page 14, line 13, with the following amended paragraph:

~~Fig. 29 is a~~ Figures 29(a)-29(b) are process diagram diagrams schematically showing the latter half of the manufacturing process for a gate bus line and a gate electrode of the active matrix substrate according to the fifth embodiment of the present invention, shown in a cross-section taken along line H-H'.

Please replace the paragraph on page 14, line 18, with the following amended paragraph:

~~Fig. 30 is a~~ Figures 30(a)-30(d) are process diagram diagrams schematically showing the manufacturing process for the active matrix substrate of the fifth embodiment of the present invention, shown in a cross-section taken along line F-F'.

Please replace the paragraph on page 14, line 22, with the following amended paragraph:

~~Fig. 31 is a~~ Figures 31(a)-31(d) are process diagram diagrams schematically showing the manufacturing process for the gate terminal of the active matrix substrate of the fifth embodiment of the present invention.

Please replace the paragraph on page 14, line 25, with the following amended paragraph:

~~Fig. 32 is a~~ Figures 32(a)-32(d) are process diagram diagrams schematically showing the manufacturing process for the drain terminal of the active matrix substrate of the fifth embodiment of the present invention.

Please replace the paragraph on page 15, line 4, with the following amended paragraph:

~~Fig. 33 is a~~ Figures 33(a)-33(d) are process diagram diagrams schematically showing the manufacturing process for the storage capacitor of the active matrix substrate of the fifth embodiment of the present invention, shown in a cross-section taken along line G-G.

Please replace the paragraph on page 15, line 23, with the following amended paragraph:

~~Fig. 39 is a~~ Figures 39(a)-39(d) are process diagram diagrams schematically showing the manufacturing process for the active matrix substrate according to the sixth embodiment of the present invention, shown in a cross-section along line I-I'.

Please replace the paragraph on page 16, line 2, with the following amended paragraph:

~~Fig. 40 is a~~ Figures 40(a)-40(d) are process diagram diagrams schematically showing the manufacturing process for the storage capacitor of the active matrix substrate according to the sixth embodiment of the present invention, shown in a cross-section along line J-J'.

Please replace the paragraph on page 16, line 24, with the following amended paragraph:

~~Fig. 47 is a~~ Figures 47(a)-47(d) are process diagram diagrams schematically showing the former half of the manufacturing process for the active matrix substrate according to the seventh embodiment of the present invention, shown in cross-section along line K-K'.

Please replace the paragraph on page 17, line 3, with the following amended paragraph:

~~Fig. 48 is a~~ Figures 48(a)-48(b) are process diagram diagrams schematically showing the latter half of the manufacturing process for the active matrix substrate according to the seventh embodiment of the present invention, shown in cross-section along line K-K'.

Please replace the paragraph on page 17, line 7, with the following amended paragraph:

~~Fig. 49 is a~~ Figures 49(a)-49(d) are process diagram diagrams schematically showing the former half of the manufacturing process for the gate terminals of the active matrix substrate according to the seventh embodiment of the present invention.

Please replace the paragraph on page 17, line 11, with the following amended paragraph:

~~Fig. 50 is a~~ Figures 50(a)-50(b) are process diagram diagrams schematically showing the latter half of the manufacturing process for the gate terminals of the active matrix substrate according to the seventh embodiment of the present invention.

Please replace the paragraph on page 17, line 15, with the following amended paragraph:

~~Fig. 51 is a~~ Figures 51(a)-51(d) are process diagram diagrams schematically showing the former half of the manufacturing process for the drain terminals 15 of the active matrix substrate according to the seventh embodiment of the present invention.

Please replace the paragraph on page 17, line 19, with the following amended paragraph:

~~Fig. 52 is a~~ Figures 52(a)-52(b) are process diagram diagrams schematically showing the latter half of the manufacturing process for the drain terminals 15 of the active matrix substrate according to the seventh embodiment of the present invention.

Please replace the paragraph on page 17, line 23, with the following amended paragraph:

~~Fig. 53 is a~~ Figures 53(a)-53(d) are process diagram diagrams schematically showing the former half of the manufacturing process for the storage capacitor of the active matrix substrate according to the seventh embodiment of the present invention, shown in cross-section along line L-L'.

Please replace the paragraph on page 17, line 3, with the following amended paragraph:

~~Fig. 54 is a~~ Figures 54(a)-54(b) are process diagram diagrams schematically showing the latter half of the manufacturing process for the storage capacitor of the active matrix substrate according to the seventh embodiment of the present invention, shown in cross-section along line L-L'.

Please replace the paragraph on page 18, line 2, with the following amended paragraph:

~~Fig. 61 is a~~ Figures 61(a)-61(d) are process diagram diagrams schematically showing the manufacturing process of the active matrix substrate according to the ninth embodiment of the present invention, shown in a cross-section along line M-M'.

Please replace the paragraph on page 18, line 6, with the following amended paragraph:

~~Fig. 62 is a~~ Figures 62(a)-62(d) are cross-sectional process diagram diagrams schematically showing the manufacturing process for a conventional active matrix substrate.